

# Publications for Swamidoss Sathiakumar

## 2018

Li, Y., Soon, J., Sathiakumar, S. (2018). Improved quadratic boost converter using cross coupled-inductor. *IEEE 4th Southern Power Electronics Conference (SPEC 2018)*, Singapore: Institute of Electrical and Electronics Engineers (IEEE). <a href="http://dx.doi.org/10.1109/SPEC.2018.8635857">[More Information]</a>

Mehrabadi, E., Sathiakumar, S. (2018). Optimal Scheduling of CHP-Based Industrial Microgrid. *2018 26th International Conference on Systems Engineering (ICSEng 2018)*, Piscataway: Institute of Electrical and Electronics Engineers (IEEE). <a href="http://dx.doi.org/10.1109/ICSENG.2018.8638167">[More Information]</a>

## 2017

Soon, J., Lu, D., Qin, L., Sathiakumar, S. (2017). Development of Multi-Mode Step Down DC/DC Converters With Fault-Tolerant Capability. *2017 IEEE 3rd International Future Energy Electronics Conference and ECCE Asia (IFEEC 2017 - ECCE ASIA)*, Kaohsiung, Taiwan: Institute of Electrical and Electronics Engineers (IEEE). <a href="http://dx.doi.org/10.1109/IFEEC.2017.7992430">[More Information]</a>

Li, Y., Sathiakumar, S. (2017). Improved Quadratic Boost Converter based on the Voltage Lift Technique. *Asia Modelling Symposium 2017: 11th Asia International Conference on Mathematical Modelling and Computer Simulation (AMS 2017)*, Kota Kinabalu, Sabah, Malaysia: Institute of Electrical and Electronics Engineers (IEEE). <a href="http://dx.doi.org/10.1109/AMS.2017.30">[More Information]</a>

Parameswaran, N., Sathiakumar, S. (2017). Knowledge Building Constructs in Elementary Textbooks. *WRFER International Conference*, India: Ernakulam International Conference.

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Lee, J., Sathiakumar, S., Shrivastava, Y. (2015). A novel stator flux and current estimator in induction motor with offsets of measured currents. *6th IEEE Symposium on Sensorless Control for Electrical Drives (SLED 2015)*, Piscataway: Institute of Electrical and Electronics Engineers (IEEE). <a href="http://dx.doi.org/10.1109/SLED.2015.7339261">[More Information]</a>

Sathiakumar, S., Parameswaran, N. (2015). A Systematic Assessment Strategy for Grading Student Answers. *26th Annual Conference of the Australasian Association for Engineering Education (AAEE 2015)*, Melbourne, Victoria: Deakin University.

## 2014

Hu, B., Sathiakumar, S. (2014). A new interleaving technique based on current ripple reduction of paralleled converters for renewable systems. *Australian Journal of Electrical and Electronics Engineering*, 11(1), 41-54. <a href="http://dx.doi.org/10.7158/1448837X.2014.11464411">[More Information]</a>

Hu, B., Sathiakumar, S. (2014). A New Interleaving Technique for Voltage Ripple Cancellation of Series-Connect Photovoltaic Systems. *Journal of Circuits, Systems and Computers*, 23(4), 1-21. <a href="http://dx.doi.org/10.1142/S0218126614500455">[More Information]</a>

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## 2012

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